

## LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
		Unit - I :				
1	21-12-15	Introduction to pnc	I	CR		
2	22-12-15	High pass RC ckt act as differentiator & sinusoidal response	I	"		
3	23-12-15	High pass RC ckt step response	I	"		
4	24-12-15	response for rate change	I	"		
5	25-12-15	response of Ramp input	I	"		
6	26-12-15	RC low pass details as an integrator & sinusoidal response	I	"		
7	27-12-15	RC low pass ckt step & rate response	I	"		
8	28-12-15	Square wave input & ramp input response	I	"		
9	29-12-15	Attenuation	I	"		
10	30-12-15	EL and RLC circuits response	I	"		
11	31-12-15	Any other ckt & problem	I	"		
		Unit - II :				
12	01-01-16	Diode clippers & series	II	"		
13	02-01-16	Two level clippers	II	"		
14	03-01-16	Series & shunt noise clippers	II	"		
15	04-01-16	Transistor clipper & emitter coupled clippers	II	"		
16	05-01-16	Comparator	II	"		
17	06-01-16	clippers & comparators	II	"		
18	07-01-16	clamping ckt & other	II	"		

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19	2-2-16	effect of diode clamping voltage	I	CR		
20	2-2-16	problems related to diode clamping	I	"		
		Unit - III:				
21	5-2-16	In diode switching time from one branch to another	II	CR		
22	8-2-16	Transistor as a switch & switching times	IV	"		
23	9-2-16	breakdown volt characteristics variation of sat. par. with temp	III	"		
24	9-2-16	Design of transistor switch	III	"		
25	12-2-16	principle of operation of Bistable multivibrator	III	"		
26	15-2-16	Analysis of fixed bias bistable	III	"		
27	16-2-16	Commutating capacitor triggering of bistable	III	"		
28	16-2-16	Schmitt trigger	III	"		
29	19-2-16	Self bias bistable bistable applications	III	"		
30	22-2-16	problems	III	"		
		Unit - IV:				
31	26-2-16	Basic principle of monostable multivibrator	IV	CR		
32	29-2-16	Analysis of collector coupled monostable multivibrator	IV	"		
33	1-3-16	Triggering in monostable multivibrator	IV	"		
34	1-3-16	Design of astable multivibrator	IV	"		
35	7-3-16	Analysis & design of astable multivibrator	IV	"		
36	8-3-16	problems	IV	"		

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37	11-3-16	Generation of sine wave signal	IV	CR		
38	14-3-16	Basic principles of miller & bootstrap low pass filter	IV	"		
39	18-3-16	Transistor miller low pass generator	IV	"		
40	18-3-16	Transistor bootstrap low pass generator	IV	"		
41	18-3-16	UTB sweep circuit	IV	"		
		Unit - V				
42	21-3-16	monostable blocking oscillator (non-trigger)	V	CR		
43	28-3-16	monostable blocking oscillator (c-miller trigger)	V	"		
44	29-3-16	Astable blocking oscillator divide circuit and	V	"		
45	29-3-16	applications	V	"		
46	1-4-16	Scrubby gate principle	V	"		
47	4-4-16	unidirectional scrubby gate	V	"		
48	5-4-16	Bidirectional scrubby gate using transistor	V	"		
49	5-4-16	Reduction of pedestal in a gate circuit	V	"		
50	8-4-16	Bidirectional diode scrubby gate	V	"		
51	11-4-16	four diode scrubby gate	V	"		
52	12-4-16	Application of scrubby gate	V	"		